

#3 0300

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

James B. Cho

Serial No: 09/801,534

Filed: March 8, 2001

For: USING PHASE INTERPOLATOR INFORMATION TO DETECT A LOSS OF LOCK CONDITION
DURING RECOVERY OF A CLOCK SIGNAL FROM A RECEIVED DATA STREAM

Docket No: TI-31746

Art Unit: 2631

Examiner: TBD

Date: May 1, 2001


LETTER TO OFFICIAL DRAFTSPERSON

Assistant Commissioner For Patents
Washington, DC 20231

Attention: Official Draftsperson

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

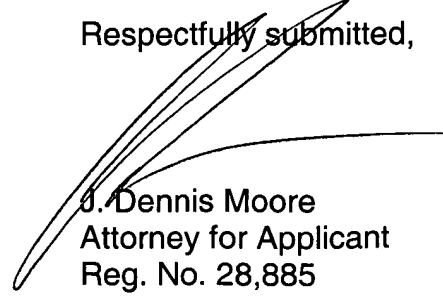
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 on 5-1-01.


Ann Trent

Dear Sir:

Transmitted herewith for filing are formal drawings for the subject case. Charge any necessary fees to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,


J. Dennis Moore
Attorney for Applicant
Reg. No. 28,885

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